 S/N 09/883795

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Leonard Forbes et al.	Examiner:	Theresa Doan
Serial No.:	09/883795	Group Art Unit:	2814
Filed:	June 18, 2001	Docket:	303.355US4
Title:	DEAPROM AND TRANSISTOR WITH GALLIUM NITRIDE OR GALLIUM ALUMINUM NITRIDE GATE		

INFORMATION DISCLOSURE STATEMENT

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement.

INFORMATION DISCLOSURE STATEMENT

Serial No :09 883795

Filing Date: June 18, 2001

Title: DEAPROM AND TRANSISTOR WITH GALLIUM NITRIDE OR GALLIUM ALUMINUM NITRIDE GATE

Page 2

Dkt: 303,355US4

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

LEONARD FORBES ET AL.

By their Representatives,

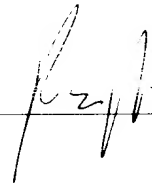
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 373-6973

Date

29 AUGUST 2003

By

Robert E. Mates
Reg. No. 35,271

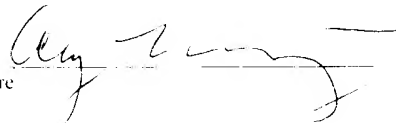


CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 29th day of August, 2003.

Name

Amy Moriarty

Signature

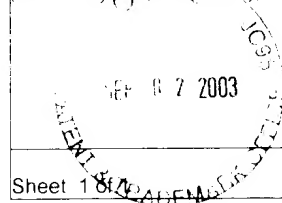


Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number	09/883795
Filing Date	June 18, 2001
First Named Inventor	Forbes, Leonard
Group Art Unit	2814
Examiner Name	Doan, Theresa

Sheet 1 of 1

Attorney Docket No: 303.355US4

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	US-4,118,795	10/03/1978	Frye, R. C., et al.	365	222	08/27/1976
	US-4,384,349	05/17/1983	McElroy, D J.	365	185.02	06/02/1980
	US-4,736,317	04/05/1988	Hu, M., et al.	364	200	07/17/1985
	US-4,816,883	03/28/1989	Baldi, Livio	357	23.5	06/22/1987
	US-4,897,710	01/30/1990	Suzuki, A., et al.	357	71	08/18/1997
	US-5,360,491	11/01/1994	Carey, P G., et al.	136	256	04/07/1993
	US-5,366,713	11/22/1994	Sichanugrist, P., et al.	423	346	05/28/1993
	US-5,425,860	06/20/1995	Truher, J. B., et al.	204	192.23	04/07/1993
	US-5,441,901	08/15/1995	Candelaria, J.	437	31	06/10/1994
	US-5,623,160	04/22/1997	Liberkowsky, J. B.	257	621	09/14/1995
	US-5,672,889	09/30/1997	Brown,	257	77	
	US-5,828,101	10/27/1998	Endo, K.	257	330	03/25/1996
	US-5,861,346	01/19/1999	Hamza, A., et al.	438	869	07/27/1995

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
--------------------	---------------------	------------------	---	-------	----------	----------------

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		NEUDECK, P., et al., "Electrical Characterization of a JFET-Accessed GaAs Dynamic RAM Cell", <u>IEEE Electron Device Letters</u> , 10(11), (1989), pp. 477-480	
		QIAN, Q., et al., "Multi-Day Dynamic Storage of Holes at the AlAs/GaAs Interface" <u>IEEE Electron Device Letters</u> , EDL-7(11), (1986), pp. 607-609	
		RUSKA, W. S., "Microelectronic Processing", <u>McGraw-Hill Book Co.</u> , (1987), 281	
		SHARMA, B., et al., "Heterojunction Devices", In: <u>Semiconductor Heterojunctions</u> , Pergamon Press, New York, (1974), pp. 133-137	
		STREETMAN, B., In: <u>Solid State Electronic Devices</u> , 4th Edition, Prentice Hall, New Jersey, (1995), pp. 217-219, 392-394	

EXAMINER**DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO 1449)

* EXAMINER: Initial if reference considered; whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. Applicant's unique citation designation number (optional). Applicant is to place a check mark here if English language Translation is attached.